

ABSTRACT

Disclosed herein is a vertical alignment liquid crystal display device with high-speed response. This vertical alignment liquid crystal display device comprises: upper and lower substrates which are disposed opposite one another at the desired interval; a liquid crystal layer sandwiched between the upper and lower substrates and formed of liquid crystals with negative dielectric anisotropy; an insulating film formed on the inner surface of the lower substrate and having a hole formed therein; a jagged pixel electrode which is formed on the insulating film in such a manner that the pixel electrode covers more than half of the hole; a counter electrode which is formed on the inner surface of the upper electrode; vertical alignment films which are interposed between the pixel electrode and the liquid crystal layer and between the counter electrode and the liquid crystal layer, respectively; and polarizers which are attached on the outer surfaces of the upper and lower substrates, respectively, in such a manner that their polarizing axes cross each other.